



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Karl M. Robinson

Title:

DEVICES HAVING IMPROVED CAPACITANCE AND METHODS OF THEIR FABRICATION

Docket No.:

303.455US3

Serial No.: 09/470,265

Filed:

December 22, 1999

Due Date: September 18, 2001

Examiner:

Cuong Q Nguyen

Group Art Unit: 2811

Box AF

Commissioner for Patents Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

 \underline{X} A return postcard.

X An Amendment and Response (10 Pages).

X A Clean Version of Pending Claims (7 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Box AF, Commissioner for Patents, Washington, D.C. 20231, on this ZO day of August, 2001.

Name

Signature

Customer Number 21186

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(GENERAL)



EXPEDITED PROCEDURE - EXAMINING GROUP 2811

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8-28-0

AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116

Box AF Commissioner for Patents Washington, D.C. 20231

In response to the Final Office Action mailed June 18, 2001, please amend the application as follows:

IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect amendment of previously pending claims 19, 20, 79, 81, 82, 84, 85, 104-106, 113, 115 and 117 and the addition of new claims 119-124. The specific amendments to individual claims are detailed in the following marked up set of claims.

- 19. (Twice Amended) A capacitor, comprising:
 - a first conductive capacitor plate of a first material;
 - a second conductive capacitor plate; and
- a dielectric interposed between said first and second conductive <u>capacitor</u> plates, wherein said dielectric is an oxide of a metal layer <u>of a second material</u> overlying the first conductive <u>capacitor</u> plate.
- 20. (Twice Amended) A memory system, comprising:
 - a monolithic memory device, comprising a capacitor, wherein the capacitor comprises:
 - a first conductive capacitor plate;
 - a second conductive capacitor plate; and